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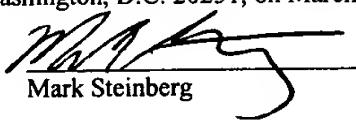
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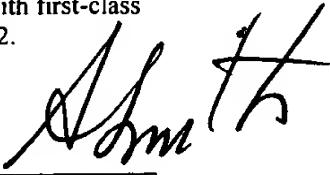
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Ferguson et al.  
Serial No: 09/575,560  
Filed: May 21, 2000  
For: METHOD AND APPARATUS FOR USE IN  
SWITCHED CAPACITOR SYSTEM  
Examiner: Phan, T.  
Art Unit: 2818

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

The undersigned hereby certifies that this document is being placed in the United States mail with first-class postage attached, addressed to Commissioner for Patents, Washington, D.C. 20231, on March 25, 2002.

  
Mark Steinberg



Commissioner for Patents  
Washington, D.C. 20231

Sir:

AMENDMENT

In response to the Office Action mailed September 25, 2001 please amend the above-identified application as follows:

In the Claims:

Please amend the following claims:

1. (Amended) A system comprising:

a DAC that receives a multi-bit digital signal and outputs at least two analog signals including a first analog signal and a second analog signal, the first analog signal being indicative of a sum of values of bits in the multi-bit digital signal, the second analog signal also being indicative of said sum of values of said bits in the multi-bit digital signal.

2. (Amended) The system of claim 1 wherein the first analog signal and the second analog signal are substantially equal to each other.

4. (Amended) A method comprising:

receiving a multi-bit digital signal; and

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